



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richard K. Williams, Michael E. Cornell, and Wai Tien Chan
Assignee: Advanced Analogic Technologies, Inc.
Title: Trench MOSFET With Recessed Clamping Diode
Serial No.: 10/606,112 Filing Date: June 24, 2003
Examiner: Matthew E. Warren Group Art Unit: 2815
Docket No.: ATI002US

San Jose, California
July 1, 2005

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call to the attention of the Examiner the following documents, which were cited in the International Search Report mailed on May 25, 2005 for the corresponding PCT Application No. PCT/US04/21211.

U.S. patents or U.S. patent application publications to which the waiver of the requirement under 37 CFR 1.98 (a)(2)(i) for submitting copies applies are not enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Authorization to charge \$180.00 for the Information Disclosure Statement fee under 37 C.F.R. § 1.17(p) is provided in an accompanying transmittal letter. The Commissioner is

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also authorized to charge any additional fees that may be required, or credit any overpayment to Deposit Account No. 50-1767.

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Respectfully submitted,



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